



10/755,732

COFC

I hereby certify that correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to: MS Post Issue, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: May 12, 2005

Signature: _____

(Michael J. Lee)

Docket No.: 30205/38071A
(PATENT)**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Letters Patent of:

Jae Jin LEE

Patent No.: 6,845,050 B2

Issued: January 18, 2005

For: SIGNAL DELAY CONTROL CIRCUIT IN A
SEMICONDUCTOR MEMORY DEVICE**Certificate
MAY 18 2005
of Correction****REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 CFR 1.322**

MS Post Issue
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted two typographical errors which should be corrected.

APPLICATION PAGE NO.	APPLICATION LINE NO.	COLUMN NO.	LINE NO.	ERROR
Amendment Filed August 10, 2004	Page 3, Line 10 Original Claim 6	Column 10 Claim 4	Line 2	PTO
Examiner's Amendment of September 15, 2004	Page 2 Re: Original Claim 12	Column 10 Claim 10	Line 53	PTO

The errors were not in the application as filed by applicant; accordingly no fee is required.

MAY 23 2005

Patent No.: 6,845,050

Docket No.: 30205/38071A

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

Respectfully submitted,

MARSHALL, GERSTEIN & BORUN LLP
6300 Sears Tower
233 South Wacker Drive
Chicago, Illinois 60606
312-474-6300

By: _____

Michael R. Hult
Registration No. 35,902
Attorney for Applicant

May 12, 2005

MAY 23 2005

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 1 of 1

PATENT NO. : 6,845,050 *B2*
APPLICATION NO. : 10/755,732
ISSUE DATE : January 18, 2005
INVENTOR(S) : Jae Jin Lee

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**Claim 4
Column 10, Line 2**

After "load element" please delete "is PMOS" and insert -- is a PMOS -- in its place.

**Claim 10
Column 10, Line 53**

After "impedance circuits" please delete "comprises;" and insert "comprises:" in its place.

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Dated: May 12, 2005

Signature: 

(Michael R. Hull)

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Michael R. Hull
MARSHALL, GERSTEIN & BORUN LLP
233 S. Wacker Drive, Suite 6300
Sears Tower
Chicago, Illinois 60606-6357

MAY 23 2005